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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

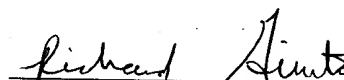
Applicant: Guillaume Royer
Serial No: 09/831,792
Confirmation No: 9624
Filed: October 18, 2001
For: SELF-ADHESIVE ELECTRONIC CIRCUIT

#9/10melt3
Approved
2/3/03

Examiner: Le, Uyen Chau N.
Art Unit: 2876

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, Washington, D.C. 20231, on the 23rd day of January, 2003.


Richard F. Giunta, Reg. No. 36,149

Commissioner for Patents
Washington, D.C. 20231

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AMENDMENT

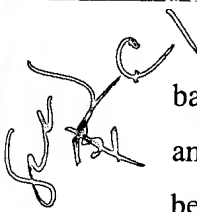
Sir:

In response to the Office Action mailed October 31, 2002, please amend the above identified application as follows:

In the Claims

Applicant presents the claims as amended below and encloses a set of marked-up claims with bracketing and underlining.

Please rewrite claims 1, 3, 4, 5 and 7 as indicated below:

- 
1. (Amended) An electronic circuit including a planar base having first and second base surfaces, an antenna attached on the first surface of the base, and a chip connected to the antenna, characterized in that a double faced adhesive is glued on one of the base surfaces, a slot being made in the double faced adhesive and the chip being arranged at least partially in the slot.